



Substitute for form 1449/PTO  <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2>  Sheet 1 of 15	<b>Complete if Known</b>														
	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>10/716,595</td> </tr> <tr> <td>Filing Date</td> <td>11/20/2003</td> </tr> <tr> <td>First Inventor</td> <td>Michael FARMWALD</td> </tr> <tr> <td>Art Unit</td> <td>2827</td> </tr> <tr> <td>Examiner</td> <td>Nguyen, Tan</td> </tr> <tr> <td>Atty Docket #</td> <td>57941.000062</td> </tr> <tr> <td>Rambus #</td> <td>RA001.2003.1.C.US</td> </tr> </table>	Application Number	10/716,595	Filing Date	11/20/2003	First Inventor	Michael FARMWALD	Art Unit	2827	Examiner	Nguyen, Tan	Atty Docket #	57941.000062	Rambus #	RA001.2003.1.C.US
Application Number	10/716,595														
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First Inventor	Michael FARMWALD														
Art Unit	2827														
Examiner	Nguyen, Tan														
Atty Docket #	57941.000062														
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Examiner Initials*	Cite No.	Document No.	Date (MM-DD- YYYY)	Patentee	Class	Subclass
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	0002	3753244	08/14/1973	Sumilas, John	340	172.5
	0003	3803562	04/09/1974	Hunter, John	340	173 R
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	0009	4250570	02/10/1981	Tsang, Frederick	365	200
	0010	4271513	06/02/1981	Maejima, Hiroshi	371	22
	0011	4317623	03/02/1982	Nobusawa, Tsukumo	354	60
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	0016	4419747	12/06/1983	Jordan, Larry	365	201
	0017	4421996	12/20/1983	Chuang, Patrick T.	307	530
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	0032	4802135	01/31/1989	Shinoda, Takashi	365	233
	0033	4814648	03/21/1989	Hynecek, Jaroslav	307	497
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	0035	4833650	05/23/1989	Hirayama, Kazutoshi	365	225.7
	0036	4903238	02/20/1990	Miyatake, Hideshi	365	189.06
	0037	4912679	03/27/1990	Shinoda, Takashi	365	230.01
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	0039	4964038	10/16/1990	Louis, Philippe	364	200
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	0042	4998222	03/05/1991	Sussman, Howard	365	203
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	0044	5018109	05/21/1991	Shinoda, Takashi	365	230.08
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	0046	5029076	07/02/1991	Magar et al.	364708	200404
	0047	5038277	08/06/1991	Altman, Barbara	364	200

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	<b>Application Number</b>	10/716,595
	<b>Filing Date</b>	11/20/2003
	<b>First Inventor</b>	Michael FARMWALD
	<b>Art Unit</b>	2827
	<b>Examiner</b>	Nguyen, Tan
	<b>Atty Docket #</b>	57941.000062
	<b>Rambus #</b>	RA001.2003.1.C.US

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Examiner Initials*	Cite No.	Document No.	Date (MM-DD- YYYY)	Patentee	Class	Subclass
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	0049	5051954	09/24/1991	Toda, Haruki	365	189.02
	0050	5051955	09/24/1991	Kobayashi, Yasuo	365	189.02
	0051	5056064	10/08/1991	Iwahashi, Hiroshi	365	233.5
	0052	5062073	10/29/1991	Masuda et al.	364	900
	0053	5075886	12/24/1991	Isobe, Mitsuo	365	194
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	0055	5124946	06/23/1992	Takahashi, Yutaka	365	195
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	0057	5181205	01/19/1993	Kertis, Robert	371	21.1
	0058	5198995	03/30/1993	Dennard, Robert	365	149
	0059	5200926	04/06/1993	Iwahashi, Hiroshi	365	233.5
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	0065	5291453	03/01/1994	Aota, Hideyuki	365	221
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	0067	5301155	04/05/1994	Wada, Tomohisa	365	201
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	0094	5929729	07/27/1999	Swarup, Arvind	333	246

**Examiner  
Signature:**

**Date Considered:**

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	<b>Application Number</b>	10/716,595
	<b>Filing Date</b>	11/20/2003
	<b>First Inventor</b>	Michael FARMWALD
	<b>Art Unit</b>	2827
	<b>Examiner</b>	Nguyen, Tan
	<b>Atty Docket #</b>	57941.000062
	<b>Rambus #</b>	RA001.2003.1.C.US

US Patent Documents						
Examiner Initials*	Cite No.	Document No.	Date (MM-DD- YYYY)	Patentee	Class	Subclass
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	0096	5936903	08/10/1999	Jeng, Terry	365	203
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	00107	6418500	07/09/2002	Gai, Weixin	710	305
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	00128	EP	EP 0028735 A2	05/20/1981	Fujitsu Fanuc Ltd.	B23Q	3/155	

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	<b>Application Number</b>	10/716,595
	<b>Filing Date</b>	11/20/2003
	<b>First Inventor</b>	Michael FARMWALD
	<b>Art Unit</b>	2827
	<b>Examiner</b>	Nguyen, Tan
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	00133	JP	JP1166545A2	06/30/1989	NEC CORP	H01L	23/50	
	00134	JP	JP2128201A2	05/16/1990	Fuji Electric Co. Ltd.	G05B	19/05	
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Non-Patent Literature Documents			
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	00139	QBM Alliance, Platform Conference, Quad Band Memory: DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002.	
	00140	PARK, JOONBAE, "A Semi-Digital Delay Locked Loop for Clock Skew Minimization" VLSI Design, 1999. Proceedings. 12th International Conference on GOA, INDIA 7-10 JAN. 1999, Los Alamitos, CA, USA, IEEE COMPUTE. SOC, US, 7 January 1999 (1999-01-07), pages 584-588, XP010319965 ISBN: 0-7695-0013-7	
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	00146	CLAYTON, PAUL R., "Prediction of Crosstalk in Ribbon Cables: Comparison of Model Predictions and Experimental Results", IEEE Transactions on Electromagnetic Compatibility, vol. EMC-20, No. 3, Aug. 1978, pp. 394-406.	

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	<b>Filing Date</b>	11/20/2003
	<b>First Inventor</b>	Michael FARMWALD
	<b>Art Unit</b>	2827
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	00147	GUMM, STEVE and DREHER, CARL T., "Unraveling the Intricacies of Dynamic RAMs," Electronic Design News, pp. 155-165 (Mar. 30, 1989). 13 pages.	
	00148	Zerbe, et al., Equalization and Clock Recovery for a 2.5 - 10 Gb/s 2-PAM/4-PAM Backplane Transceiver Cell. Presented at ISSCC 2003, paper 4.6 2 pages.	
	00149	ALFKE, PETER, "Efficient Shift Registers, LFSR Counters, and Long Pseudo-Random Sequence Generators." Xilinx Application Note, XAPP 052, (version 1.1), July 7, 1996. 6 pages.	
	00150	DALLY, WILLIAM J. and POULTON, JOHN W., Digital Systems Engineering, Cambridge University Press, New York, NY, pp. 334-347 and 352, 1998.	
	00151	LEBECK, ALVIN et al., "Power Aware page Allocation", Proceeding of the Ninth International Conference on Architectural Support for Programming Languages and Operating Systems", (ASPLOS-IX), Nov. 2000, 12 pages.	
	00152	Invalidity argument materials of potential licensee relating to claim 23 of U.S. Patent No. 6,038,195, to which present U.S. Patent Application No. 10/939,501 claims priority and is related by virtue of the fact that both claim priority to U.S. Patent Application No. 07/510,8998 (now abandoned), citing J.L. Rodengen, The Legend of Amdahl, Write Stuff Syndicate, Inc., 2000, and FACOM M-780, Operating Manual, including two separate English language translations thereof.	
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	00154	SHAH, SUNAY et al., "A Temperature Independent Trimmable Current Source." Department of Engineering Science, University of Oxford. ISCAS 2002. 4 pages.	
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	00157	Invalidity argument materials of potential licensee relating to claim 1 of U.S. Patent No. 6,034,918, which is related to present U.S. Patent Application No. 10/939,501 by virtue of the fact that both claim priority to U.S. Patent Application No. 07/510,898 (now abandoned), citing J.L. Rodengen, The Legend of Amdahl, Write Stuff Syndicate, Inc., 2000, and FACOM M-780, Operating Manual, including two separate English language translations thereof.	
	00158	GUTIERREZ, GERMAN and DESIMONE, DAN, "Integrated PLL Clock Generator for 275 MHz Graphic Displays," IEEE Custom Integrated Circuits Conference, 1990, pp. 15.1.1-15.1.4.	
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	00160	CLEMENT, PATRICK, "Process-Tolerant High Level Shifter", Motorola Technical Developments, Motorola Inc., 1987.	
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	<b>Filing Date</b>	11/20/2003
	<b>First Inventor</b>	Michael FARMWALD
	<b>Art Unit</b>	2827
	<b>Examiner</b>	Nguyen, Tan
	<b>Atty Docket #</b>	57941.000062
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	00162	Invalidity argument materials of potential licensee relating to claim 1 of U.S. Patent No. 6,034,918, which is related to present U.S. Appl. No. 10/939,500 by virtue of the fact that both claim priority to U.S. Appl. No. 07/510,898 (now abandoned), citing J.L. Rodengen, The Legend of Amdahl, Write Stuff Syndicate, Inc., 2000, and FACOM M-780, Operating Manual, including two separate English language translations thereof.	
	00163	TAKAI, et al., "250 Mbyte/s Synchronous DRAM Using a 3-Stage-Pipelined Architecture," IEEE Journal of Solid-State Circuits, vol. 29, No. 4, pp. 426-429, (Apr., 1994).	
	00164	Opposition against European patent 0 525 068, (Application No. 91 908 374.1-2201) (German & English Translation), 04/14/2004	
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	00166	LU, NICKY et al., "The Future of DRAMS", ISSCC 88, 02-18-2004, pp 1-2 (w/conference, copyright pages)	
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	00169	KUSHIYAMA, N. et al., "500Mbyte/sec Data-Rate 512 Kbits x 9 DRAM Using a Novel I/O Interface," 1992 Symposium on VLSI Circuits Digest of Technical Papers, 1992	
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	00171	International Preliminary Examination Report from PCT Application No. PCT/US91/02590	
	00172	Patent Owner's Response to Office Action of August 25, 2008 in Reexamination of Farmwald, et al., U.S. Patent 6,697,295, Control No. 95/001,013 dated October 24, 2008	
	00173	Exh. B - Decision on Petition dated June 30, 2005, cited in Patent Owner's Response from Reexamination Control No. 90/007,355 dated October 24, 2008	
	00174	Exh. E-7 - Excerpts from Trial Testimony of Joel Karp, dated April 6, 2006 from Hynix Semiconductor Inc. v. Rambus Inc., No. 00-20905 (N.D. Cal.) cited in Patent Owner's Response from Reexamination Control No. 90/007,355 dated October 24, 2008.	
	00175	Exh. E-8 - Excerpts from Trial Testimony of Mark Horowitz, dated April 12, 2006 from Hynix Semiconductor Inc. v. Rambus Inc., No. 00-20905 (N.D. Cal.) cited in Patent Owner's Response from Reexamination Control No. 90/007,355 dated October 24, 2008.	
	00176	Exh. E-11 - Excerpts from Trial Transcript, dated April 13, 2003 from Hynix Semiconductor Inc. v. Rambus Inc., No. 00-20905 (N.D. Cal.) cited in Patent Owner's Response from Reexamination Control No. 90/007,355 dated October 24, 2008.	
	00177	Exh. G-1 - Excerpts from Trial Testimony of Howard Sussman, dated May 8, 2003 from In re Rambus, No. 9302 (F.T.C.) cited in Patent Owner's Response from Reexamination Control No. 90/007,355 dated October 24, 2008.	
	00178	Exh. G-2 - Excerpts from Trial Testimony of Bruce Jacob, dated June 17, 2003 from In re Rambus, No. 9302 (F.T.C.) cited in Patent Owner's Response from Reexamination Control No. 90/007,355 dated October 24, 2008.	

<b>Examiner Signature:</b>		<b>Date Considered:</b>	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **Applicant is to place a check mark here if English language Translation is attached.			

